

Optimization of electrical conductivity of SnS thin film of $0.2 < t \leq 0.4 \mu\text{m}$ thickness for field effect transistor application

T. O. Daniel^{a,*}, U. E. Uno^b, K. U. Isah^c and U. Ahmadu^d

^a*Department of Physics/Geology/Geophysics, Alex Ekwueme-Federal University Ndufu-Alike, Ikwo, Abakaliki, Ebonyi state, Nigeria.*

^{*}*e-mail: danielojonugwathomas@gmail.com*

^{b,c,d}*Department of Physics, Federal University of Technology, Minna, Nigeria.*

Received 7 September 2020; accepted 30 November 2020

This study is focused on the investigation of SnS thin film for transistor application. Electron traps, associated to the grain boundary effect, alter the electrical conductivity of the SnS semiconductor thin film, thereby militating on the attainment of the threshold voltage required for operation as a transistor. SnS semiconductor thin films of 0.20, 0.25, 0.30, 0.35, 0.40 μm thickness were deposited using aerosol assisted chemical vapour deposition on glass substrates. Surface profilometry, Scanning electron microscope, Energy dispersive X-ray spectroscopy and Hall measurements were used to characterize the thickness, microstructure, elemental composition and electrical properties of the SnS thin films. SnS thin films were found to consist of Sn and S elements whose composition varied with increase in thickness. The film conductivity was found to vary with grain size and grain boundary, which is a function of the film thickness by itself. The SnS film of 0.4 μm thickness shows optimal grain growth with a grain size of 130.31 nm signifying an optimum for the as-deposited SnS films as the larger grains reduce the number of grain boundaries and charge trap density allowing charge carriers to move freely in the lattice, thereby causing a reduction in resistivity and increase in conductivity of the films which is essential for obtaining the threshold voltage for a transistor semiconductor channel layer. The carrier concentration of $1.438 \times 10^{13} \text{ cm}^{-3}$, conductivity of $2.768 \times 10^{-6} (\Omega\text{cm})^{-1}$ due to low resistivity of $3.612 \times 10^5 \Omega\text{cm}$ of 0.4 μm SnS thin film thickness is optimum and favours the attainment of the threshold voltage for a field effect transistor operation; hence, making possible the application of SnS thin films as semiconductor channel layers in a field effect transistor.

Keywords: Conductivity; grain boundary; grain size; SnS; thin film.

PACS: 72.20.-i; 98.38.Bn; 73.50.-h; 79.60.Dp; 81.05.Dz

DOI: <https://doi.org/10.31349/RevMexFis.67.263>

1. Introduction

The functionality of a field effect transistor with low operating voltage and high carrier concentration depends on the semiconductor channel layer material and the dielectric gate amidst other parameters because a transistor's threshold voltage (V_{th}) depends on the semiconductor material [1].

However, so far only oxides, nitrides, carbon nanotubes and organic semiconductors have realized the recorded modulation of electronic states in a field effect transistor. The application of the electric double layer field effect transistor (FET) technique to other groups of semiconductors is of wide interest for its performance on novel devices [2]. Tin (II) sulphide (SnS) is of interest as a semiconductor channel layer for transistors. SnS is a readily available Tin chalcogenide layered semiconductor in group IV-VI with a layered orthorhombic structure [3]. SnS contains no poisonous or costly elements; it is of type p with a hole mobility of $1.4 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ and carrier concentration on the order of 10^{16} cm^{-3} [4]. SnS thin film is largely unexplored for use in field effect transistors as the literature relating to the use of SnS as a semiconductor channel layer is relatively scarce.

Therefore, SnS thin film is a worthy material to investigate the feasibility of the chalcogenide family for a field effect transistor's channel and to determine the optimum depo-

sition parameters that will provide the required properties for its usage. Thin film thickness, which is a function of the film microstructure, is a parameter of interest. In semiconductor processing, the regulation of film thickness is critical as the film thickness influences the grain size and grain boundary which is necessary for defining the electrical properties of a semiconductor material [5]. Voids in a separate island-like state and traps associated with certain film thickness affect charge carriers due to the number of small grains with large quantities of grain boundaries acting as an electron trap, affecting an SnS film's electrical conductivity and the threshold voltage for use in a FET.

The method of deposition of interest is the aerosol assisted chemical vapour deposition (AACVD). AACVD provides the comparative benefit of using less-volatile precursors, thus increasing the types of molecules which can be used to deposit thin films. For the field effect transistor a 0.20-0.40 μm thickness range was selected based on the average values gotten from the literature.

2. Experimental procedure

Preliminary deposition and film thickness measurements were performed to establish the concentrations of the starting precursor. SnS semiconductor thin films were deposited

using 0.1 M Tin chloride dehydrates and 0.2 M of Thiourea which were weighed in stoichiometric proportion and dissolved in ethanol solvent. The two solutions were mixed and stirred for 1 hour using a magnetic stirrer at room temperature after which the resulting solution was filtered through a 0.22 μm syringe filter and then deposited on the substrate by AACVD at a constant substrate temperature of 258°C, nozzle distance of 6.8 mm, substrate to nozzle distance of 3 cm, spray volume of 0.2 mL and spray rate of 0.04 ml/min. Five samples of SnS thin films were deposited at thickness of 0.4 μm , 0.35 μm , 0.30 μm , 0.25 μm and 0.20 which was achieved by keeping the initial concentration of the SnS precursors constant using 1 ml of the precursor while varying the concentration of the ethanol solvent through 0 ml, 0.5 ml, 1.0 ml, 1.5 ml, and 2.0 ml.

Structural characterisation of the films was carried out at room temperature using an X-ray diffractometer (D8 Advance, Bruker AXS, 40Kv, 40 mA) with monochromatic $\text{CuK}\alpha$ ($\lambda = 1.540598 \text{ \AA}$) while phase identification was done using the inorganic crystal structure data pattern [6].

The average crystallite size (D) of the film was calculated using Debye Scherer's formula:

$$D = \frac{0.9\lambda}{\beta \cos \theta}, \quad (1)$$

where β full width at half maximum (FWHM), θ diffraction angle, k Shape factor and λ wavelength of the X-rays (1.5406 \AA).

Dislocation density δ was calculated using [7]:

$$\delta = \frac{1}{D^2}. \quad (2)$$

and the micro-strain ε was estimated using the equation [8,9]

$$\varepsilon = \frac{\beta}{4 \tan \theta}. \quad (3)$$

The morphology and microstructure of the SnS thin films were characterized using High Resolution Scanning Electron Microscopy (HR-SEM, Zeiss) while the elemental composition of the film was determined by an Energy dispersive X-ray spectroscopy (EDS; Oxford instrument). A Profilometry (VEECO DEKTAK 150) was used to carry out measurements of the thickness of the deposited films. The carrier density, carrier mobility and carrier type were determined by an ECOPIA Hall Effect measurement system (HMS 3000 Hall measurement system) based on a Van der Pauw configuration.

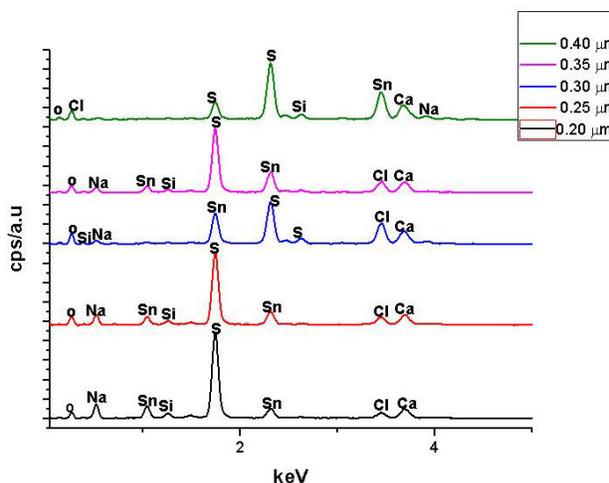


FIGURE 1. EDS spectrum of the SnS thin films of 0.20 μm , 0.25 μm , 0.30 μm , 0.35 μm and 0.40 μm thickness.

3. Results and discussion

3.1. Compositional analysis of As-deposited SnS thin films

The thickness of the as deposited SnS thin films as-obtained from surface profilometry were 0.20 μm , 0.25 μm , 0.30 μm , 0.35 μm and 0.40 μm . Figure 1 displays the EDS spectrum of the as-deposited SnS thin films, while the elemental composition of the SnS films is shown in Table I in terms of their atomic percentage. Sn and S were found to be dominant in SnS thin films, while small quantities of sodium (Na), calcium (Ca), silicon (Si), oxygen and chlorine (Cl) were also found in films that could be linked to the soda lime glass substrate used for film deposition which was similarly reported by [10]. From Table I, it was observed that the Sulphur content increased with increased film thickness. Lower film thickness compared to higher thickness has lower Sulphur content.

Since Sulphur has a greater vapour pressure compared to Tin, more of the Sulphur atoms will arrive on the substrate compared to Tin atoms as observed in Table I. Some of the Sulphur atoms appear to be reflected back on arrival at the substrate surface which could be due to adsorbed-atom mobility, lower sticking coefficient or heat radiant effect be-

TABLE I. As-deposited SnS thin film composition in terms of atomic percent (at.%).

Thickness (μm)	Sn (at. %)	S (at. %)	Ca (at. %)	Na (at. %)	Cl (at. %)	Si (at. %)	O (at. %)	TOTAL
0.20	36.43	45.57	8.35	1.04	5.62	1.74	1.25	100
0.25	35.72	46.28	8.35	1.04	5.62	1.74	1.25	100
0.30	33.19	48.81	8.35	1.04	5.62	1.74	1.25	100
0.35	30.75	51.25	8.35	1.04	5.62	1.74	1.25	100
0.40	29.85	52.15	8.35	1.04	5.62	1.74	1.25	100

TABLE II. Summary of calculated structural parameters for different SnS thin film thickness.

S/n	Thickness (μm)	2θ ($^\circ$)	Full width half maximum β ($^\circ$)	Crystal size D (nm)	Dislocation density $\delta \times 10^{14}$ (Lines/ m^2)	Micro strain $\varepsilon \times 10^{-3}$
1	0.40	31.739	0.136	60.72	2.71	2.09
2	0.35	31.742	0.139	59.41	2.83	2.13
3	0.30	31.757	0.143	57.75	3.00	2.19
4	0.25	31.703	0.146	56.56	3.13	2.24
5	0.20	31.702	0.150	55.05	3.30	2.31

tween the substrate surface and the deposited SnS thin film resulting in lower Sulphur content or a significant deficiency at lower thickness than higher thickness. However, the effect decreases at higher thickness due to the drop in the loss of scattering leading to a decline in heat gradient with the formation of clusters of SnS crystallites at higher SnS thin film thickness which is consistent with the report of Ref. [11].

3.2. X-Ray Diffraction

Figure 2 shows the XRD pattern of SnS thin films of $0.20 \mu\text{m}$, $0.25 \mu\text{m}$, $0.30 \mu\text{m}$, $0.35 \mu\text{m}$ and $0.40 \mu\text{m}$ thickness scan over the range of $20^\circ \leq 2\theta \leq 80^\circ$. The peaks observed for the films were analysed and indexed using the standard pattern with orthorhombic structured SnS (JCPDS PDF card 39-0354) for the mineral herzenbergite. Analysis of the films' XRD pattern shows the formation of a loosely crystalline film because of the presence of few peaks. The films of 0.20 and $0.25 \mu\text{m}$ thickness showed only one peak at a 2θ value of 31.7° diffracted along the (040) plane that appears as a single crystal in nature or a single broad, poorly crystalline peak with other peaks fully suppressed, which could be due to the ordering of short range between the atoms at the thickness level and is peculiar to chalcogenides deposited at room temperature of low crystallinity or an almost amorphous nature [12]. The SnS thin films of 0.30 , 0.35 and $0.40 \mu\text{m}$ thickness, however, showed few peaks at 2θ approximately 31.7° and

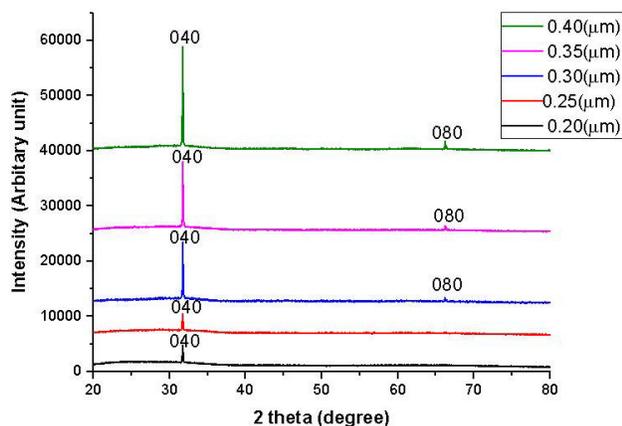


FIGURE 2. XRD pattern for SnS thin films of $0.20 \mu\text{m}$, $0.25 \mu\text{m}$, $0.30 \mu\text{m}$, $0.35 \mu\text{m}$ and $0.40 \mu\text{m}$ thickness.

66.6° diffracted along the plane (040) and (080) respectively, indicating an increase in atomic order with increase in film thickness. There is no difference in films peak position with thickness increase; but there is an increase in intensity with a rise in film thickness. The broad peak located at a small angle of diffraction is unique to the amorphous network. The full width half maximum of the peaks were estimated using a Gaussian function via the use of OriginPro 2015 software. The summarised structural parameters of the films is given in Table II.

The average size of the crystallites was estimated using the full width at half the maximum of the most preferred orientation (040), which is also the Scherer formula's most extreme value. The average size of crystallites was found to increase with increase in the thickness of the SnS thin films. Increasing the film thickness facilitates the formation of grown atomic layers, creating a thermal energy gain system which enhances atomic restructuring leading to an increase in the crystal size and a decline in crystal defects in the SnS thin films. From Table II, the decline in full width at half maxima with increase in the film thickness represents the decrease in the amount of lattice imperfections in the deposited SnS thin film due to a substantial decrease in the SnS thin film's internal micro strain. The average crystallite size of the films increased from 55.05 nm to 60.72 nm with a film thickness increase from 0.2 to $0.40 \mu\text{m}$. The low value of 55.05 nm for the lower thickness of $0.2 \mu\text{m}$ could be attributable to a decline in the growth of deposited SnS film atoms due to a reduction in nuclide formation caused by the insufficiency of material and bonding between the nuclides and the substrate surface which could limit the mobility of deposited atoms in part. However, nuclide formation significantly increases with increase in SnS film thickness and a decrease in the interactions between the substrate surface and the deposited film layer, thereby resulting in the merger of smaller crystallites into larger ones as a consequence of the potential energy gap between small and large particles. Similar results were published in [13,14]. Increase in film thickness increases the size of crystallite by forming larger grains on the surface of smaller grains [15]. The thickness of $0.40 \mu\text{m}$ has the largest size of crystallite, and could therefore be the most crystalline. The decline in micro strain and dislocation density with increased film thickness could be due to the stress release in

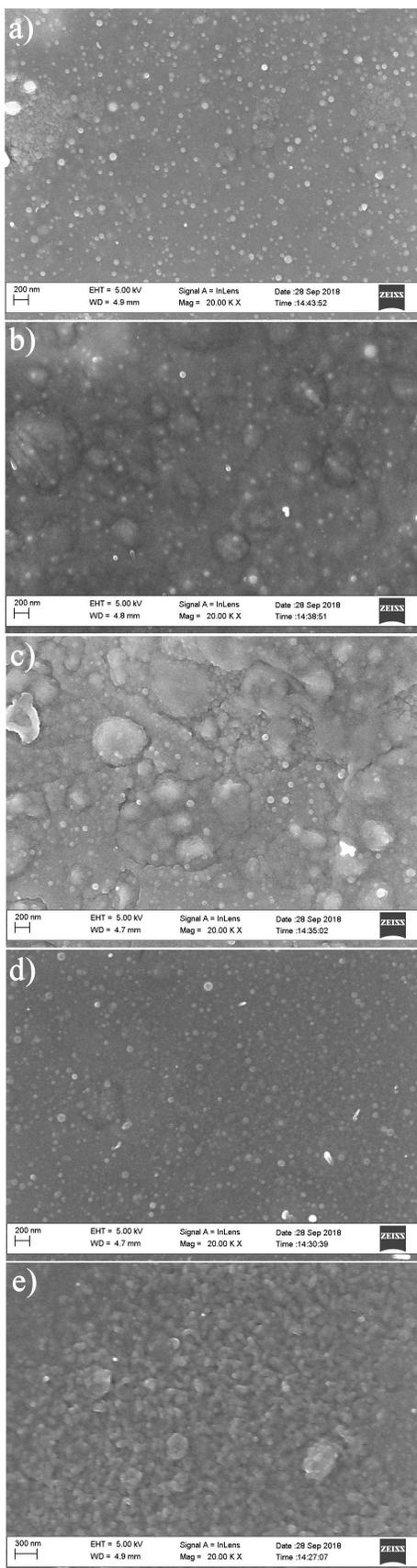


FIGURE 3. (a-e). The surface morphology of the as-deposited SnS thin films of 0.2, 0.25, 0.30, 0.35 and 0.40 μm thickness.

SnS thin films by simultaneous growth annealing phenomenon contributing to improved film crystallinity with increase in the SnS thin film thickness.

3.3. Scanning electron microscopy (SEM)

Figure 3 (a-e) shows the surface morphology of the 0.2, 0.25, 0.30, 0.35 and 0.40 μm thickness of as-deposited SnS thin films as investigated with SEM. Using imagej software [16], the average grain size of the SnS thin films was computed.

The SnS thin films of 0.2 to 0.40 μm thickness exhibited two distinctive microstructures consisting of a section of agglomerates and some defined spherical grains as shown in Fig. 3. The agglomerates increase for films of 0.2 to 0.30 μm thickness after which they decrease for films of 0.35 to 0.40 μm thickness resulting to a more specified grain structure and growth of SnS thin film of 0.40 μm thickness. The 0.2 to 0.30 μm thickness of the SnS films displayed uniformly spaced grain sizes. No visible crack or hole was observed in the deposited SnS thin films as the films were located randomly on the SnS film surface. With increase in SnS film thickness to 0.35 and 0.40 μm , the films contain more uniform and closely bound grains such that the film of 0.40 μm thickness possess the most enhanced grain and crystal structure. In addition, with increase in film thickness, the deeper layers of the film atoms are exposed to stronger interatomic forces leading to the creation of a compact structure, while the atoms near the surface are subjected to weaker interatomic forces to form a loosely packed grain structure for a thinner film [12,15]. Figure 4 displays the composite average grain size plot for thicknesses of 0.2, 0.25, 0.30, 0.35 and 0.40 μm .

Figure 4 shows that, with increase in the SnS thin film thickness, the average grain size as labelled on each column increases. The increase in thickness allowed the growth of smaller grains into larger grains that could be credited to the

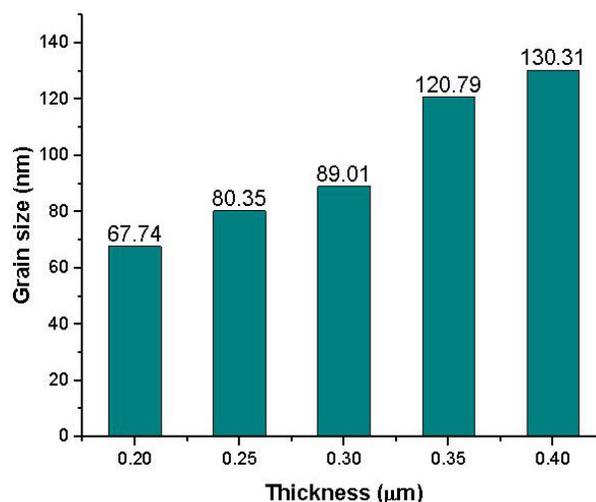


FIGURE 4. Composite average grain size plot for 0.20, 0.25, 0.30, 0.35 and 0.40 μm SnS thin film thickness.

TABLE III. Electrical parameters of SnS thin films of 0.20, 0.25, 0.30, 0.35 and 0.40 μm SnS film thickness.

Thickness (μm)	Bulk concentration N_b (cm^{-3})	Average Hall coefficient R_H (cm^3/c)	Carrier mobility μ (cm^2/Vs)	Resistivity ρ (Ωcm)	Conductivity ρ (Ωcm) $^{-1}$
0.20	2.572×10^9	2.224×10^9	5.257×10^2	4.230×10^6	2.364×10^{-7}
0.25	2.807×10^9	2.427×10^9	9.132×10^2	2.658×10^6	3.762×10^{-7}
0.30	2.990×10^9	2.642×10^9	1.714×10^3	6.081×10^5	1.645×10^{-6}
0.35	1.438×10^{10}	4.342×10^9	2.063×10^3	4.085×10^5	2.448×10^{-6}
0.40	2.238×10^{10}	6.928×10^9	4.337×10^3	3.612×10^5	2.768×10^{-6}

coalescence of small grains into larger grains with an apparent change in the more tightly packed grain size of the SnS thin film.

3.4. Hall effect measurement of 0.20, 0.25, 0.30, 0.35 and 0.40 μm SnS film thickness

The electrical properties of the as deposited SnS thin film as a function of film thickness is shown in Table III.

From the table, the average Hall coefficient of all the deposited SnS thin films was found to be positive, which means that all the films possess conductivity of the p-type carrier. A similar form of thin film conductivity was reported by [18-20] among others for thin films of SnS. Several researchers have reported variations in the electrical resistivity of deposited SnS thin films to depend on lattice defects (*e.g.* interstitials, vacancies and the presence of binary film phases), residual stress, and grain size. However, no binary phases of SnS film have been found from the EDS studies but the grain size of the deposited films increases with thickness increase [12,15,18,19]. Therefore the difference in electrical conductivity with thickness may largely be due to the grain size of the films. The high resistivity of 0.20 and 0.25 μm thickness for thin films may be due to the existence of higher number of lattice defects, low crystallinity and the existence of inter-crystalline regions affecting grain growth to a relatively large scale. A further increase in thickness from 0.30 μm to 0.40 μm leads to a progressive change of the film crystallinity resulting in a more uniform structure with comparatively larger grains. The larger grains minimize the number of grain boundaries and trap density thereby enabling the carriers to move freely in the lattice resulting in reduced resistivity and increased film conductivity. The presence of defects acts as scattering centres for thinner films that could improve the creation of trap states capable of trapping carriers and the number of free carriers available for conduction. After trapping mobile carriers, the carrier traps may become electrically charged by generating a possible energy barrier that hinders the movement of charge carriers from one crystallite

to another, thus reducing carrier mobility and the number of available carriers available for electrical conduction [14,15].

4. Conclusion

The SnS thin films deposited by AACVD were found to be polycrystalline consisting of Sn and S elements in varying composition. The presence of large grain size with increases in the SnS film thickness from 0.20 μm to 0.40 μm reduces the number of grain boundaries and charge trap density, hence allowing charge carriers to move freely in the lattice leading to a reduction in resistivity and an increase in conductivity of the film. This also reveals that the attainment of the threshold voltage of a transistor is proportional to the conductivity of the semiconductor channel layer which is a function of the presence of grain size and grain boundary interactions within the semiconductor channel layer. The high carrier concentration of $1.438 \times 10^{13} \text{ cm}^{-3}$ due to low resistivity of $3.612 \times 10^5 \Omega\text{cm}$ of 0.4 μm SnS thin film is optimum and favours the attainment of the threshold voltage for a transistor operation, hence it indicates the possibility of usage or application of SnS thin film as a semiconductor channel in a field effect transistor at an optimum charge carrier conductivity of $2.768 \times 10^{-6} (\Omega\text{cm})^{-1}$, 0.4 μm thickness and 130.31 μm grain size.

Acknowledgments

This research did not receive any specific grant from funding agencies in the public, commercial, or not-for-profit sectors. However, the authors acknowledged the Staff and management of Alex Ekwueme Federal University Ndufu Alike Ikwo, Ebonyi state, Federal University of Technology Minna, Sheda Science and Technology (SHESTCO), Namiroch research laboratory Abuja, iThemba Laboratory South Africa and the electron microscopy unit of the University of Western Cape, South Africa where the laboratory preparation of the film/ device and characterisations were carried out.

1. H. Du, X. Lin, Z. Xu, and D. Chu, Electric double-layer transistors: a review of recent progress, *J. Mater. Sci.* **50** (2015) 5641, <https://doi.org/10.1007/s10853-015-9121-y>.
2. H. Yuan et al., Liquid-Gated Ambipolar Transport in Ultrathin Films of a Topological Insulator Bi_2Te_3 , *Nano Lett.* **11** (2011) 2601, <https://doi.org/10.1021/nl201561u>.
3. P. Thiruramanathan, G. S. Hikku, R. Krishna Sharma, and M. Siva Shakthi, Preparation and characterization of indium doped SnS thin films for solar cell applications, *Int. J. TechnoChem Res.* **1** (2015) 59.
4. H. Soonmin, A. Kassim, and T. Weetee, Thickness Dependent Characteristics of Chemically Deposited Tin Sulfide Films, *Univ. J. Chem.* **1** (2013) 170.
5. T. Daniel, U. Uno, K. Isah, and U. Ahmadu, Electric Double Layer Field Effect Transistor Using SnS Thin Film as Semiconductor Channel Layer and Honey Gate Dielectric, *East Eur. J. Phys.* **3** (2019) 71, <https://doi.org/10.26565/2312-4334-2019-3-09>.
6. A. Sugaki, A. Kitakaze, and H. Kitazawa, Synthesized Tin and Tin-Silver Sulfide Minerals. Synthetic Sulfide Minerals (XIII), *Sci. Rep. Tohoku Univ.* **16** (1985) 199.
7. S. Ilican, Y. Caglar, and M. Caglar, Preparation and characterization of ZnO thin films deposited by sol-gel spin coating method, *J. Optoelectron. Adv. Mater.* **10** (2008) 2578.
8. S. M. Ahmed, L. A. Latif, and A. Kh. Salim, The Effect of Substrate Temperature on the Optical and Structural Properties of Tin Sulfide Thin Films, *J. Basrah Res.* **37** (2011) 57.
9. J. Lv et al., Effect of annealing temperature and CuO on microstructure and optical band gap of $\text{Cu}_x\text{Zn}_{1-x}\text{O}$ thin films, *Superlattice Microstruct.* **61** (2013) 115, <https://doi.org/10.1016/j.spmi.2013.06.010>.
10. A. Gómez et al., A Study of the Structural, Optical and Electrical Properties of SnS Thin Films Modified by Plasma, *J. Mater. Sci. Eng. B* **3** (2013) 352.
11. P. Prathap, N. Revathi, Y. P. Venkata Subbaiah, and K. T. Ramakrishna Reddy, Thickness effect on the microstructure, morphology and optoelectronic properties of ZnS films, *J. Phys.: Condens. Matter* **20** (2007) 035205, <https://doi.org/10.1088/0953-8984/20/03/035205>.
12. P. Jain and P. Arun, Parameters influencing the optical properties of SnS thin films, *J. Semicond.* **34** (2013) 093004, <https://doi.org/10.1088/1674-4926/34/9/093004>.
13. M. Devika et al., Influence of annealing on physical properties of evaporated SnS films, *Semicond. Sci. Technol.* **21** (2006) 1125, <https://doi.org/10.1088/1674-4926/34/9/093004>.
14. E. Guneri et al., Properties of p-type SnS thin films prepared by chemical bath deposition, *Chalcogenide Lett.* **7** (2010) 685.
15. A. Jain, P. Sagar, and R. M. Mehra, Changes of structural, optical and electrical properties of sol-gel derived ZnO films with their thickness, *Mater. Sci. Poland* **25** (2007) 233.
16. W. Rasband, ImageJ, National Institute of Mental Health, Bethesda, USA, <https://imagej.nih.gov/ij/index.html>.
17. G. Julio, M. D. Merindano, M. Canals, and M. Ralló, Image processing techniques to quantify microprojections on outer corneal epithelial cells, *J. Anat.* **212** (2008) 879, <https://doi.org/10.1111/j.1469-7580.2008.00898.x>.
18. T. Srinivasa Reddy and M. C. Santhosh Kumar, Co-evaporated SnS thin films for visible light photodetector applications, *RSC Adv.* **6** (2016) 95680, <https://doi.org/10.1039/C6RA20129F>.
19. C. Gao and H. Shen, Influence of the deposition parameters on the properties of orthorhombic SnS films by chemical bath deposition, *Thin Solid Films* **520** (2012) 3523, <https://doi.org/10.1016/j.tsf.2011.12.077>.
20. T. Daniel, U. Uno, K. Isah, and U. Ahmadu, Tuning of SnS Thin Film Conductivity on Annealing in an Open Air Environment for Transistor Application, *East Eur. J. Phys.* **2** (2020) 94, <https://doi.org/10.26565/2312-4334-2020-2-08>.