Electrical characterization of GaN/ALN heterostructures grown by molecular beam epitaxy on silicon substrates

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In this paper, \( n \)-type gallium nitride thin films were grown on \( p \)-type and \( n \)-type silicon substrates by Molecular Beam Epitaxy technique, employing an aluminum nitride layer as an insulator buffer coating. The samples constitute primary semiconductor-insulator-semiconductor or SIS heterostructures, on which silver electrical contacts were deposited superficially by sputtering for electrical characterization purposes. The current intensity vs. voltage curves, charge carriers density, diffusion coefficient and mobility of charge carriers were determined in darkness conditions. Under AM1.5 homogenous illumination, the samples exhibited relatively large open-circuit voltage values, but low values for energy conversion efficiencies. Additionally, the dependency in photon energy of the open-circuit voltage was determined, in the range 1.77 eV to 4.13 eV, showing a minimum in the energy corresponding to the GaN energy band gap. Finally we discuss the prevailing charge transfer mechanism.

Keywords: Electrical characterization; gallium nitride; Hall effect; molecular beam epitaxy; semiconductor-insulator-semiconductor structures

PACS: 73.40 Ty; 73.50 Pz; 73.61 Ey; 06.60 Ei

1. Introduction

The study of III-N semiconductors alloys family, particularly nitrides of indium, gallium and aluminum (InN, GaN, and AlN) has become an exciting research area because of its wide technological applications. Due to the values of their energy band gaps (0.7 eV, 3.5 eV and 6.3 eV respectively), the growth of multilayer thin films and nanostructures systems, and/or their ternary alloys allows to explore or research both, narrow and wide wavelength ranges, from the mid infrared (mid-IR) to deep ultraviolet (deep-UV) regions [1,2]. Among the most prominent applications are: Radiation detection, light emitting diodes (LED’s) and laser diodes (LD’s), photovoltaic (PV) cells and solid state lighting, for example [3-5]. For the study of the physical properties, such as thermal, optical, and electrical properties, as well as for the structural and chemical characterization of the III-N semiconductor family, a variety of characterization techniques and methods are commonly used, such as: Photoluminescence (PL) response measurements, Photothermal (PT) and Photoacoustic Spectroscopy (PAS) characterization techniques, X-ray diffraction (XRD), Reflection High-Energy Electron Diffraction (RHEED), Scanning Electron Microscopy (SEM), Energy-Dispersive X-ray Spectroscopy (EDS), Hall Effect measurements (HEM), among others. These techniques are non-invasive, no destructive tests and do not demand special sample preparation, with the exception of the electrical characterizations which needs the deposition of Ohmic electrical contacts.

The Molecular Beam Epitaxy (MBE) of GaN directly in Si substrates presents several difficulties, like the formation of SiN amorphous layer at the interface due to the strong reaction of the Nitrogen atoms with the Si substrate [6,7]. In order to overcome this problem intermediate buffer layer, such as AlN, have been employed leading to an improvement of the structural and optical properties of the GaN [8]. In this work, we report the electrical characterization of GaN films grown by MBE on silicon (Si) wafers employing an AlN thin buffer. The electrical properties of GaN/AlN/Si heterostructures were studied by means of Hall Effect and Current Intensity vs. Voltage (I-V) curves measured at room temperature.

2. Samples preparation

Three groups of samples, labeled here as M1, M2, and M3, of thin films of GaN were grown by the MBE technique in
ELECTRICAL CHARACTERIZATION OF GaN/AlN HETEROSTRUCTURES GROWN BY MOLECULAR BEAM EPITAXY.

TABLE I. Growth parameters used for the group sample.

<table>
<thead>
<tr>
<th>Sample</th>
<th>T_{sub} °C</th>
<th>T_{Ga} °C</th>
<th>d_{GaN} μm</th>
<th>P_{Ga} \times 10^7 torr</th>
<th>P_{MBE}</th>
<th>T_{sus} °C</th>
<th>T_{Al} °C</th>
<th>d_{AlN} nm</th>
<th>P_{Al} \times 10^7 torr</th>
</tr>
</thead>
<tbody>
<tr>
<td>M1</td>
<td>750</td>
<td>970</td>
<td>0.6</td>
<td>4.8</td>
<td>45</td>
<td>850</td>
<td>1133</td>
<td>46.0</td>
<td>3.3</td>
</tr>
<tr>
<td>M2</td>
<td>730</td>
<td>970</td>
<td>0.6</td>
<td>4.8</td>
<td>40</td>
<td>850</td>
<td>1133</td>
<td>46.0</td>
<td>3.3</td>
</tr>
<tr>
<td>M3</td>
<td>800</td>
<td>972</td>
<td>1.4</td>
<td>5.4</td>
<td>48</td>
<td>850</td>
<td>1127</td>
<td>27.6</td>
<td>3.3</td>
</tr>
</tbody>
</table>

As substrates, a 380 μm thickness low-resistivity p-type silicon (Si) was used for samples M1 and M2; meanwhile for sample M3, a 500 μm thickness high-resistive, n-type Si wafer was employed. All the substrates had the (111) orientation, and were previously characterized by HEM, in darkness conditions at room temperature. On each of the substrates, an AlN layer was grown as insulator buffer layer, previous to the growth of the GaN layer, unintentionally doped as n-type due to native N-vacancy defects [9-11], and a possible diffusion of Si donors from the substrate to the AlN and GaN layers, during the growth process [12]. In Table I, the used growth parameters are reported.

Here, T_{sub} is the substrate temperature; T_{Ga} and T_{Al} are the temperatures of the Knudsen cells of Ga, and Al cells, respectively; and d_{GaN} and d_{AlN} are the nominal thickness of the GaN and AlN layers respectively. PMBE is the pressure of the MBE chamber during growth; and PGa and PAI are the beam equivalent pressure of the Knudsen cells of Ga and Al, respectively. From each case, square sections (with an area of 0.25 cm²) were cut using a diamond cutting wheel. The samples were cleaned, firstly in cyclohexane, secondly in acetone (95%), after that in isopropyl alcohol (95%) and finally washed in deionized water (DI), in an ultrasonic bath during 15 minutes for each step. For all samples, square-shaped silver (Ag) contacts were deposited, near as possible to the corners of the samples, directly on the surface of the GaN layer, using a sputtering deposition system (Den
ton Desk IV Sputter). The deposition time was 1.5 minutes, obtaining electrical contacts with thickness in the range of 15 nm to 22 nm, and having an area of ∼1 mm². Also, a thin layer of Ag was deposited (by the same method) on the exposed back surface of the Si substrates. The upper and lower contacts allow electrical measurements in the GaN layer and across the axial direction of the sample. The electrical contacts shows good mechanical adherence and shape definition. Figure 1 shows a schematic drawing of the samples structure, and the positions of the electrical contacts.

From spectral reflectance measurements in the range of 350 nm to 700 nm, the average thickness of the GaN layer was determined [13,14] for each sample group, finding differences less than 7% between the nominal thicknesses and those obtained by reflectance measurements.

3. Electrical characterizations

Two kinds of electrical characterizations were performed at room temperature: HEM and I-V measurements of the substrate and GaN layers in darkness conditions, to obtain their individual electrical parameters; and I-V and open-circuit voltages measurements, under homogenous illumination conditions, to evaluate the energy conversion efficiency and spectral response, of the samples heterostructures.

3.1. Electrical measurements: Darkness conditions

By means of a Hall Effect Measurement System (ECOPIA, HMS-3000) the electrical parameters of the GaN and substrate were determined, using the van der Pauw geometry [15], applying a current intensity of 1 mA in the presence of a uniform magnetic flux density of 0.55 T. Since the measurements were done in darkness conditions, there is no influence of photogenerated charge carriers in the results. Tables II and III summarize the results from the HEM characterization.

Where, n_c, \mu_c, \rho, R_H and D_c stands for the charge carrier concentration, charge mobility, electrical resistivity, average Hall coefficient and charge carrier diffusion coefficient, respectively. From Tables II and III, it can be seen that the GaN layers exhibit larger charge carrier concentration and lower electrical resistivity values than their correspond-
TABLE II. Hall Effect results of the GaN layer of the samples group, in darkness condition at room temperature.

<table>
<thead>
<tr>
<th>GaN layer</th>
<th>M1</th>
<th>M2</th>
<th>M3</th>
</tr>
</thead>
<tbody>
<tr>
<td>( n_c )</td>
<td>( 8.3 \times 10^{18} )</td>
<td>( 6.0 \times 10^{18} )</td>
<td>( 1.1 \times 10^{18} )</td>
</tr>
<tr>
<td>( \mu_c )</td>
<td>17.3</td>
<td>29.1</td>
<td>34.2</td>
</tr>
<tr>
<td>( \frac{\mu_c}{(V_s)} )</td>
<td>4.4 \times 10^{-2}</td>
<td>3.6 \times 10^{-2}</td>
<td>1.6 \times 10^{-1}</td>
</tr>
<tr>
<td>( \rho )</td>
<td>1.44</td>
<td>0.75</td>
<td>0.88</td>
</tr>
<tr>
<td>( \frac{1}{\rho} )</td>
<td>0.75</td>
<td>-1.0</td>
<td>-5.6</td>
</tr>
<tr>
<td>( D_c )</td>
<td>0.44</td>
<td>0.75</td>
<td>0.88</td>
</tr>
</tbody>
</table>

Table III. Hall Effect results of the substrates, in darkness condition at room temperature.

<table>
<thead>
<tr>
<th>Substrates</th>
<th>( n_c )</th>
<th>( \mu_c )</th>
<th>( \frac{\mu_c}{(V_s)} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>p-type</td>
<td>1.5 \times 10^{15}</td>
<td>8.4 \times 10^{2}</td>
<td>6.6 \times 10^{3}</td>
</tr>
<tr>
<td>n-type</td>
<td>1.1 \times 10^{12}</td>
<td>1.6 \times 10^{3}</td>
<td>3.4 \times 10^{3}</td>
</tr>
</tbody>
</table>

Table IV summarizes the open-circuit voltages \( (V_{oc}) \), the short-circuit currents \( (I_{sc}) \), the series resistance \( (R) \), the maximum electrical power \( (W_{max}) \), and the energy conversion efficiency \( (\eta) \) of the samples, under AM1.5 illumination.

3.2. Electrical measurements: Homogenous illumination conditions

To obtain the electrical response of the samples under homogenous illumination, two sets of data are here reported: (a) The I-V curves under AM1.5 illumination, and (b) The open-circuit voltage dependence on the excitation wavelength. Figure 3 schematizes the experimental setups employed.

The AM1.5 illumination was acquired by a solar simulator (Oriel-Newport mod. 91160), with an irradiance of 140 W cm\(^{-2}\). The monochromatic illumination was acquired by passing the output radiation from a 900 W Xe arc lamp, through a monochromator. In Figs. 4 and 5, the obtained I-V curves are presented.

As can be seen from Figs. 4 and 5, the samples M1 and M2 have similar response (since are almost twin samples). On the other hand, the sample M3 reaches higher values for the open-circuit voltage and electric power, even when its substrate shows a greater resistivity value (almost 300 fold to the corresponding value to the \( p \)-type substrates). Possibly, the combination of a thinner buffer layer, and higher values of \( \mu_c \) and \( D_c \) in M3 sample and its substrate, improve the charge transfer in this case.

Figure 2. I-V curves of samples M1 (half-full squares), M2 (half-full circles), and M3 (half-full up triangles), through AB trajectory.
TABLE IV. Results of the Photovoltaic response measurements under homogenous illumination.

<table>
<thead>
<tr>
<th></th>
<th>$V_{oc}$</th>
<th>$I_{sc}$</th>
<th>R</th>
<th>$W_{max}$</th>
<th>$\eta$</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>mV</td>
<td>$\mu$A</td>
<td>$\Omega\cdot cm^2$</td>
<td>$\mu$W</td>
<td>%</td>
</tr>
<tr>
<td>M1</td>
<td>57.8</td>
<td>60.6</td>
<td>230.5</td>
<td>0.9</td>
<td>$2.6 \times 10^{-6}$</td>
</tr>
<tr>
<td>M2</td>
<td>61.2</td>
<td>53.2</td>
<td>290.0</td>
<td>0.8</td>
<td>$2.4 \times 10^{-6}$</td>
</tr>
<tr>
<td>M3</td>
<td>132.9</td>
<td>104.1</td>
<td>287.5</td>
<td>3.6</td>
<td>$1.0 \times 10^{-5}$</td>
</tr>
</tbody>
</table>

FIGURE 4. I-V curves of samples M1 (empty squares), M2 (empty circles), and M3 (empty triangles).

FIGURE 5. Electrical Power of samples M1 (empty squares), M2 (empty circles), and M3 (empty triangles).

The extremely low values of the short-circuit currents and conversion efficiencies can be explained as result of the large values of the thickness of the buffer layer of the samples. Simaschevici et al. [18] mentioned, in their review work, the great importance of the thickness of the insulator layers to achieving good energy conversion efficiencies in photovoltaic cells. As an example of it, Simaschevici et al. reports in the aforementioned paper that for $n^+\text{ITO}/\text{SiO}_2/n\text{Si}$ PV cells, the conversion efficiency varies from 1% to 13% for buffer layers with thickness around some tens of Angstroms, being tunneling the principal mechanism for charge transfer. In our case, the thickness of the buffer layers are two orders of magnitude larger, which makes less probably the tunneling of the photogenerated charge carriers.

The results summarized in Table IV supports the idea of a charge transfer mechanism different from tunneling, for the samples under study. It is our impression that the principal charge transfer mechanism is given by the existence of an internal, time-varying displacement electric field, creating a small drift current intensity which reflects in the series resistance values. The origin of such displacement electric field should lie in the diffusion, and in the bulk and surface recombination of the photogenerated charge carriers in GaN and substrate layers. Finally, in Fig. 6, the open-circuit voltage for samples M1 and M2 is shown as function of the photon energy.

As the previous figure indicates, there is a clear dependency of the open-circuit voltage in the photon energy. It can be seen in Fig. 6, that the open-circuit voltage diminishes as the photon energy increases, falling down dramatically for energies close to the GaN band gap energy (3.5 eV), and increasing for higher energies.

For lower energies than 3.1 eV, the typical values for the absorption coefficient of the GaN are in the order of 102 cm$^{-1}$, increasing rapidly (from a minimum value) for photon energy values up to 3.5 eV [19,20]. For the same photon energy range, the substrate increases monotonically their absorption coefficient [21] as the energy increases. Considering the behavior of the absorption coefficients of the GaN and Si layers, for sufficiently small values of the photon energy, the time variations of displacement electric field is due to the recombination of the photogenerated charge carriers in the substrate layer. As the photon energy gets higher, the
open-circuit voltage signal drops because the GaN layer begins to absorb a significant portion of the light, diminishing the light intensity absorbed by the substrate. When the photon energy gets higher values than the GaN band gap energy, more and more photogenerated charge carriers are generated in the GaN layer, and therefore, the open-circuit voltage signal is due to bulk and surface recombination of photogenerated charge carriers in the GaN and Si layers, increasing again the open circuit voltage signal. The spectral response of the open-circuit voltage indicates that these heterostructures could be used as radiation detector.

4. Conclusions

By means of molecular beam epitaxy technique, samples of GaN/AlN/Si were grown under different conditions and substrates. The Hall Effect measurements and I-V curves (in darkness condition) shown that even for unintentional doping of the layers, the charge carrier concentrations reach relatively large values, causing an Ohmic response of the gallium nitride layer, for the applied current intensity range. Additionally, the I-V curves under AM1.5 illumination shows that the samples form SIS structures with very low energy conversion efficiency, due to the thickness of the buffer layer which does not allow the tunneling of the charge carriers, being probably the presence of a time-varying displacement electric field the most likely charge transport mechanism. It is possible that diminishing the thickness of the buffer layer to some nanometers, the energy conversion efficiency will improve. Further work in this direction is in progress.

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